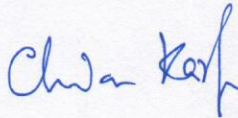# TEQIP-III Sponsored 5-day Short Term Course on C-Based VLSI Design: Synthesis, Optimization and Verification
April 1 - 5, 2019

List of Selected Participants

Sl. No.	Name	Institute	Category	Application Status	Remarks
1	Jayshree	NIT, Nagaland	TEQIP	Selected	
2	Vivek Dubey	GEC, Ajmer	TEQIP	Selected	
3	Anshul Malik	UEC, Ujjain	TEQIP	Selected	
4	Srinivasa T M	NIT, Calicut	TEQIP	Selected	
5	Anjali Bharti	ABVGIET, Shimla	TEQIP	Provisionally Selected	Hardcopies of application not received yet
6	Akanksha Sharma	ABVGIET, Shimla	TEQIP	Provisionally Selected	Hardcopies of application not received yet
7	Anshika Salaria	ABVGIET, Shimla	TEQIP	Provisionally Selected	Hardcopies of application not received yet
8	Nidhish Antony	DUIET, Dibrugarh	TEQIP	Selected	
9	Ravi Kumar	ABVGIET, Shimla	TEQIP	Provisionally Selected	Hardcopies of application not received yet
10	Mridul Jyoti Roy	AEC, Assam	TEQIP	Selected	
11	Varun Kumar Kakar	BTKIT, Dwarahat	Non-TEQIP	Provisionally Selected	Hardcopies of application not received yet
12	Kumar Saurabh	IPR, Gandhinagar	Non-TEQIP	Provisionally Selected	Hardcopies of application not received yet
13	Priyanka Panigrahi	IIT, Guwahati	Non-TEQIP	Selected	
14	Ramanuj Chouksey	IIT, Guwahati	Non-TEQIP	Selected	
15	Surajit Das	IIT, Guwahati	Non-TEQIP	Selected	
16	Adem Mohammed Abderehman	IIT Guwahati	Non-TEQIP	Selected	

Note: We will update the provisionally selected participants once we received their applications.

15/03/2019

सहायक प्राध्यापक
संगणक विज्ञान एवं अभियांत्रिकी विभाग
Assistant Professor